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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Complete if Known

Application Number	Unknown
Filing Date	Even Date Herewith
First Named Inventor	Derner, Scott
Group Art Unit	Unknown
Examiner Name	Tran, Michael

Sheet 1 of 1

Attorney Docket No: 303.767US2

## US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
MS	US-6,215,714	04/20/2001	Takemae, Yoshihiro , et al.	365	222	03/02/2000
MS	US-6,349,068	02/19/2002	Takemae, Yoshihiro , et al.	365	222	04/10/2001

## FOREIGN PATENT DOCUMENTS

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T <sup>2</sup>
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## OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
MS		PRINCE, BETTY , "Semiconductor Memories", <u>Wiley, 2nd Edition</u> , (1983),pp. 220-221	

EXAMINER

M. Tran

DATE CONSIDERED

8/9/04

Substitute Disclosure Statement Form (PTO-1449)

\* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. Applicant's unique citation designation number (optional): Applicant is to place a check mark here if English language Translation is attached